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Γ	APPLICATION NO.	FILING DATE	DATE FIRST NAMED INVENTOR			ATTORNEY DOCKET NO.
_	09/599,718	06/22/00	CELII		F	TI-29276
	- JACQUELINE J GARNER		IM52/0821		EXAMINER	
					BROWN:	. C
	TEXAS INSTR				ART UNIT	PAPER NUMBER
	PO BOX 6554				1765	3
	DALLAS TX	75265			DATE MAILED:	08/21/01

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks



Office Action Summary

Application No.

Applicant(s)

09/599,718

Examiner Charlotte A. Brown

Kraft et al. Art Unit 1765

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<u>-</u>	The MAILING DATE of this communication appears	on the cover sheet with the correspondence address				
THE M - Extens afte - If the be - If NO cor - Failure - Any re ear	PRTENED STATUTORY PERIOD FOR REPLY IS SE IAILING DATE OF THIS COMMUNICATION. Sions of time may be available under the provisions of 37 of the may be available under the provisions of 37 of the mailing date of this communication for reply specified above is less than thirty (30) day considered timely. Period for reply is specified above, the maximum statutory munication.	CFR 1.136 (a). In no event, however, may a reply be timely filed ication. In a reply within the statutory minimum of thirty (30) days will reprise the period will apply and will expire SIX (6) MONTHS from the mailing date of this by statute, cause the application to become ABANDONED (35 U.S.C. § 133). The mailing date of this communication, even if timely filed, may reduce any				
		ction is non-final.				
2a) ☐ 3) ☐	This details is the resulting for allowance except for formal matters, prosecution as to the merits is					
Disposi	tion of Claims	is/are pending in the application.				
4) 💢	Claim(s) <u>1-9</u>	is/are withdrawn from consideration.				
4	la) Of the above, claim(s)	is/are withdrawn from consideration.				
5) 🗌	Claim(s)	is/are rejected				
6) 💢	Claim(s) <u>1-9</u>	is/are rejected.				
7) 🗆	Claim(s)	is/are objected to.				
8) 🗆	Claims	are subject to restriction and/or election requirement.				
	The specification is objected to by the Examiner. The drawing(s) filed onis/t	are objected to by the Examiner. is: a) approved b) disapproved.				
13)⊡ a)	Acknowledgement is made of a claim for foreign All b) Some* c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Election for a list of the certified action for a list of the certified copies of the priority application from the International Election for a list of the certified action for a list o	have been received. have been received in Application No y documents have been received in this National Stage sureau (PCT Rule 17.2(a)).				
14)	the state of a claim for dome	stic priority under 35 U.S.C. § 119(e).				
	Motice of References Cited (PTO-892)	18] Interview Summary (PTO-413) Paper No(s).				
	Notice of Draftsperson's Patent Drawing Review (PTO-948)	19) Notice of Informal Patent Application (PTO-152)				
	Information Disclosure Statement(s) (PTO-1449) Paper No(s).	20) Other:				

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DETAILED ACTION

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-9 rejected under 35 U.S.C. 103(a) as being unpatentable over Jang et al. (US 6,019,906) in view of Chen et al. (US 6,211,061).

Jang teaches a hard masking method for forming a patterned microelectronics layer within a microelectronics fabrication. A semiconductor substrate is provided. A blanket first dielectric layer is formed on the semiconductor substrate. The dielectric layer may be formed from any of several dielectric materials that are conventional in the art (Column 10, lines 46-50). This reads on the applicant's limitation of forming an interlevel dielectric layer over a semiconductor body.

Patterned conductor layers are formed over the blanket first dielectric layer. A blanket inter-metal dielectric layer is formed over the substrate. The layer is formed from an oxygen containing plasma etchable material which is a low dielectric constant material. The materials may include but are not limited to organic polymer spin-on-polymer dielectric materials (Column 11, lines 32-50). A hard mask layer is formed over the structure. A series of patterned photoresist layers are formed (Column 12, lines 12-30). This reads on the applicant's-limitation of forming a via pattern over the hard mask. A first plasma etch is employed to etch through the hard mask layer (Column 12, lines 51-57). A second plasma etch is performed to etch the blanket inter-metal dielectric

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layer (Column 13, lines 7-20). This reads on the applicant's limitation of extending the via by selectively etching the intrametal dielectric layer.

Unlike the claimed invention, Jang does not teach methods for depositing a BARC layer over the hard mask and within the via, forming a trench pattern over the BARC layer, and etching a trench in the intrametal dielectric layer.

Chen teaches a method for forming a dual damascene structure in a carbon-based, low-K dielectric naterial. A low-k dielectric layer is formed over a substrate. A hard mask layer is formed over the dielectric layer (Column 6, lines 13-17). A BARC layer is formed over the hard mask layer (Column 6, lines 38-44). Figure 5A shows that the BARC layer (40) is formed over the hard mask layer and within the via. The BARC layer, a second barrier layer, and the low-k dielectric layer are patterned to form a via opening. A photoresist mask, having an opening over the intended location of the via opening, is formed over the BARC layer. This reads on the applicant's limitation of forming a trench pattern over the BARC layer. The photoresist masks and the BARC layer are removed. This extends the via opening through the low-K dielectric layer. This reads on the applicant's limitation of etching a trench in the intrametal dielectric layer. Figure 6A shows that the BARC layer is completely removed from over the hard mask and in the via. This reads on the applicant's limitation that the etching step removes at least a portion of the BARC layer within the via.

It is the Examiner's position that a person having ordinary skill in the art would have found it obvious to modify Jang with the methods of depositing a BARC layer over the hard mask

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and within the via, forming a trench pattern over the BARC layer, and etching a trench in the intrametal dielectric layer as taught by Chen. These additional steps would have been anticipated in order to from a trench in the intrametal dielectric layer.

- 3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. (US 5,858,807, US 6,235,633, and US 6,245,669)
- 4. Any inquiry concerning this communication from the Examiner should be directed to Charlotte A. Brown whose telephone number is (703) 305-0727.

CAB

August 17, 2001

FELISA HITESHEW PRIMARY EXAMINER